

## ANALYSIS, DESIGN & SIMULATION OF ANALOG TO DIGITAL CONVERTER

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### **ABSTRACT**

*In this work, I successfully designed the 4-bit SAR ADC and 12 bit cyclic ADC and successfully measured the parameters of ADC by non-metrics curve and quantization curve. My motive was to design a 12-bit SAR ADC but because of clock skew, and signal integration in different sub-blocks, I could have designed the 4-bit SAR ADC.*

*The Sub-blocks of SAR ADC and Cyclic ADC are 80 % same that's why I completed 12 bit Cyclic ADC, in which there is less problem of clock skew, and signal integration. All building blocks of SAR and Cyclic ADC were successfully simulated.*

**KEYWORDS:** *Simulation of Analog, Digital Converter*